

FIG. 1

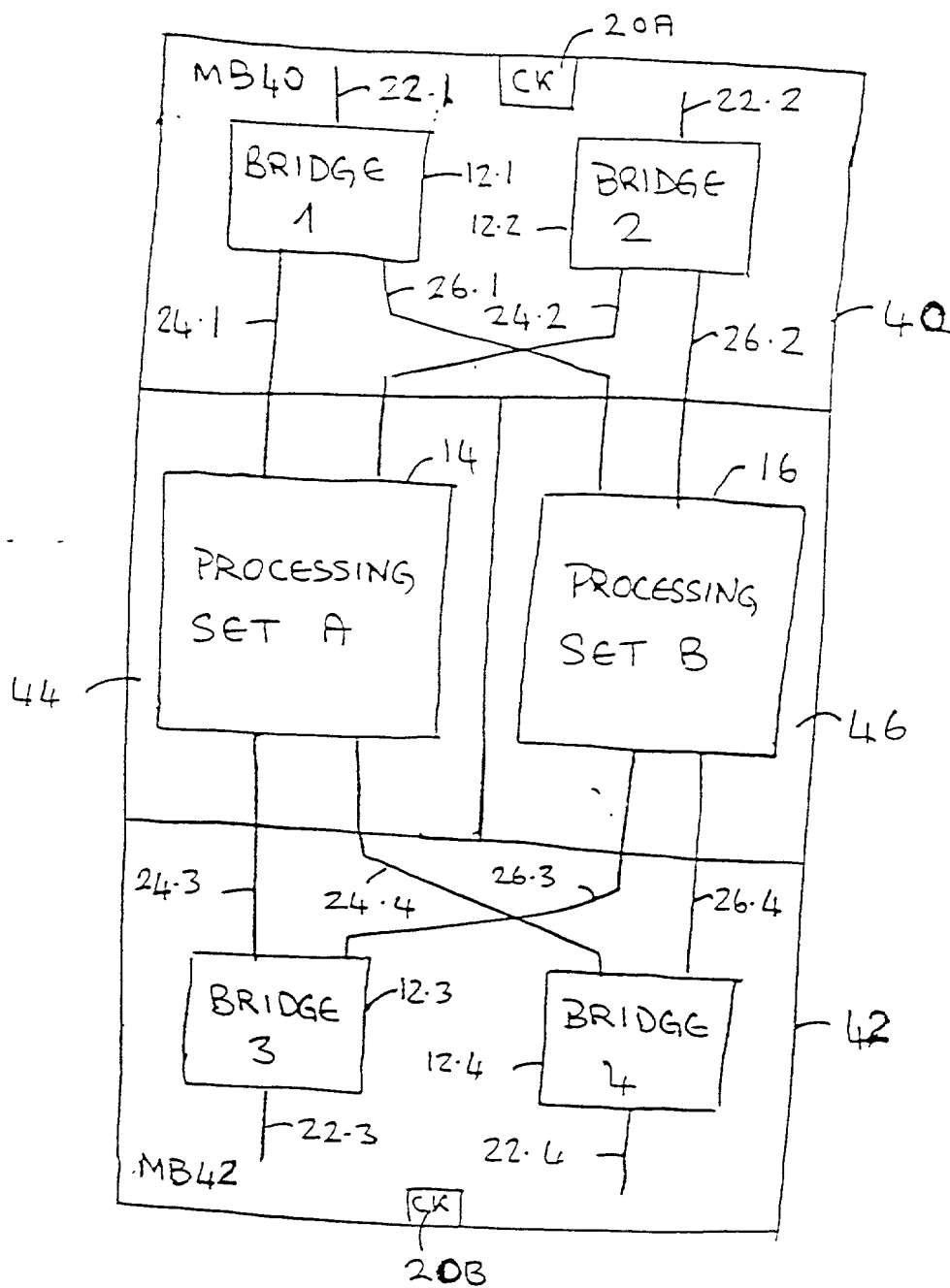
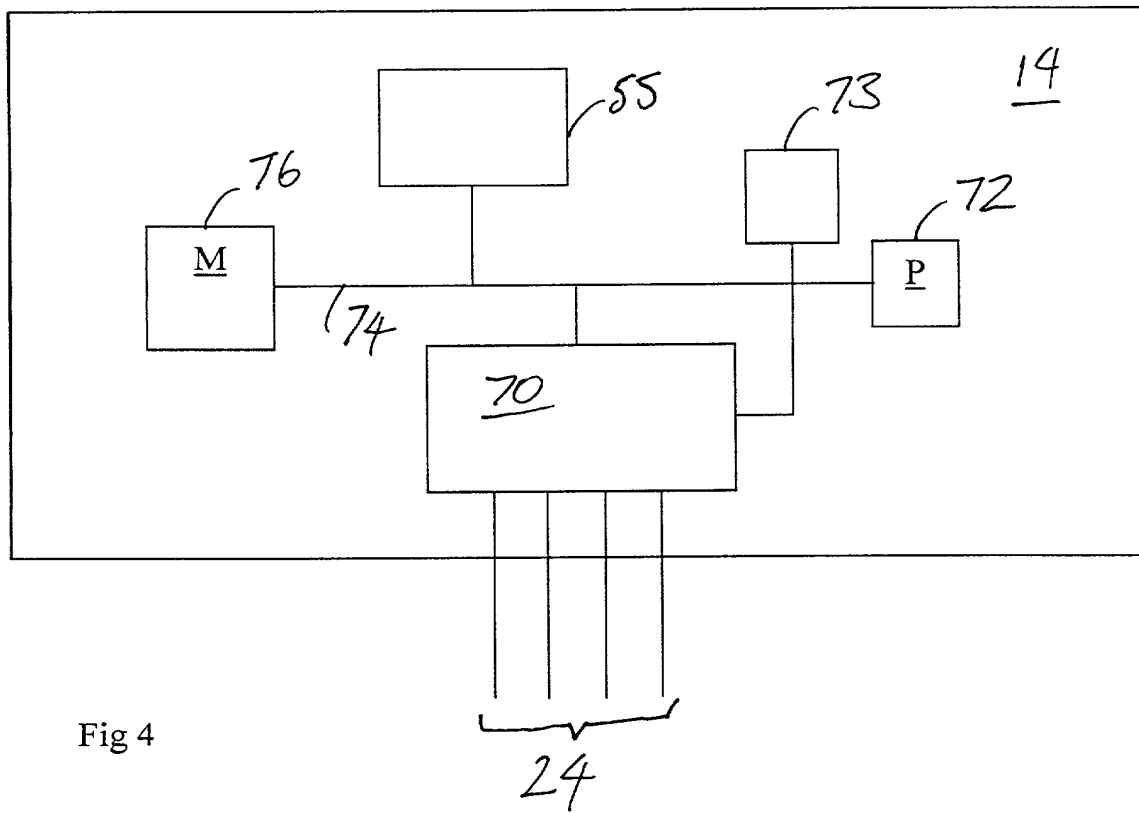
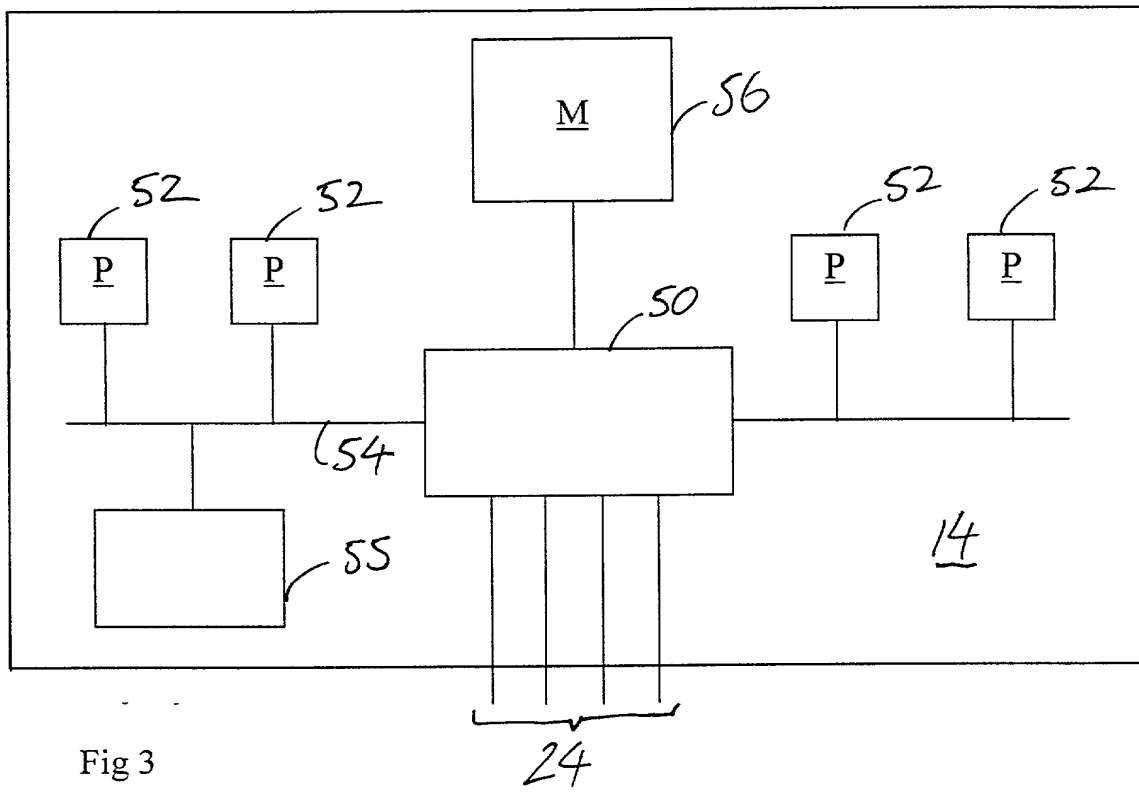


FIG. 2

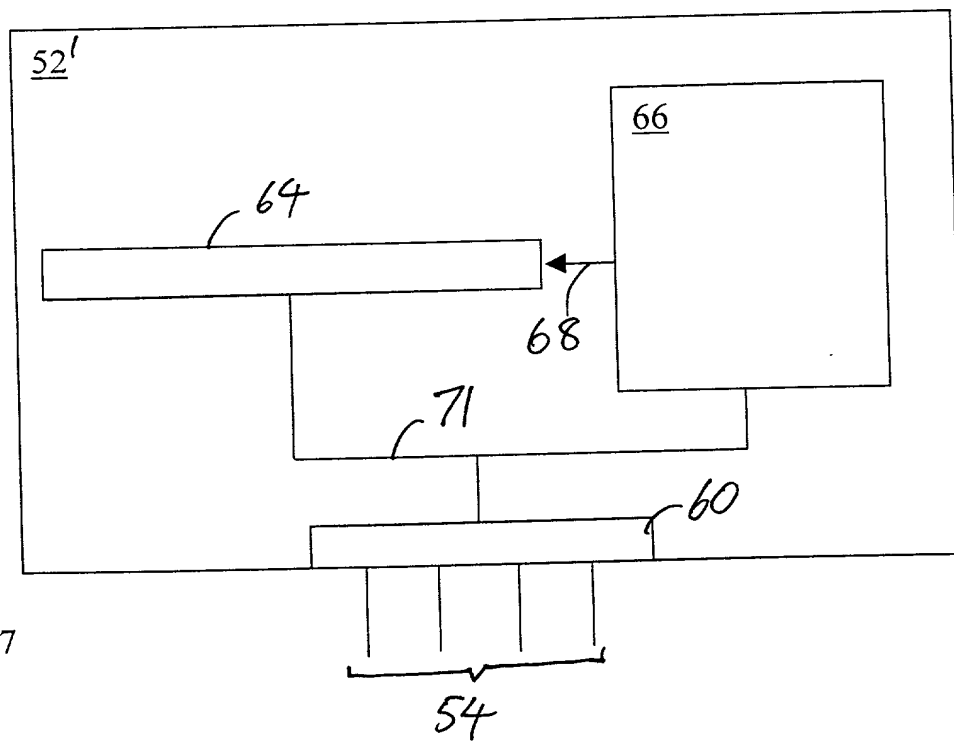
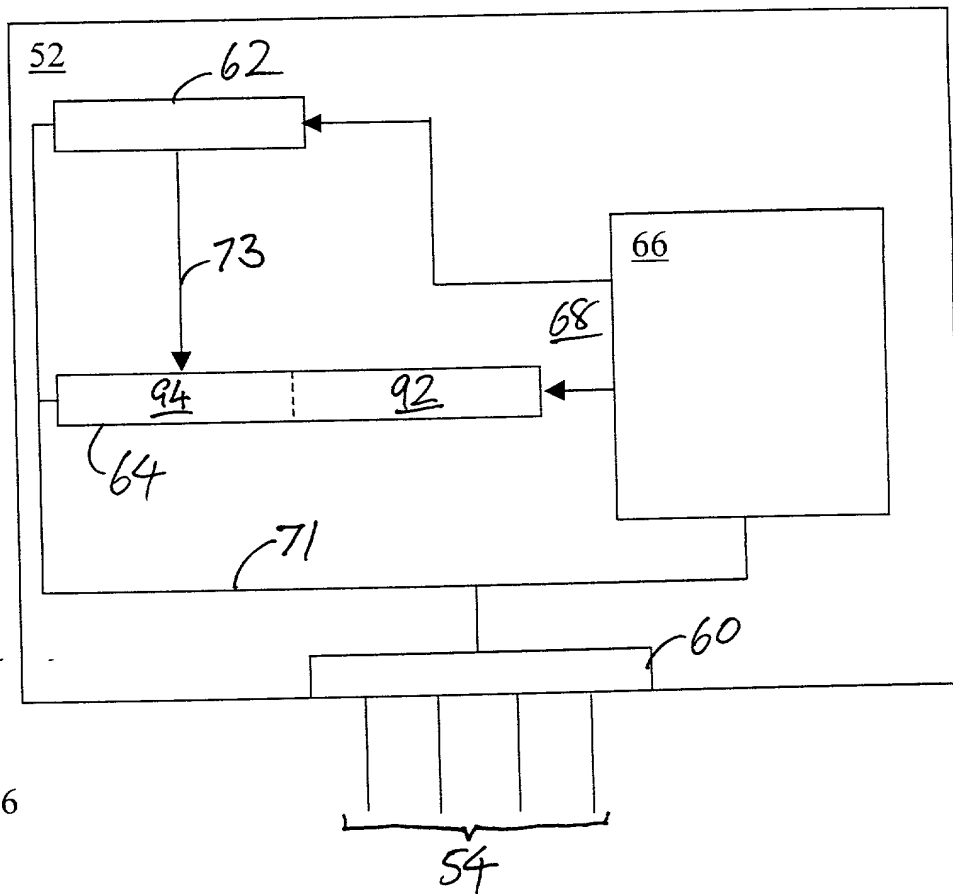


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Bits	Field	Use	RW
<63:48>	<i>manuf</i>	Manufacturer identification	R
<47:32>	<i>impl</i>	Implementation identification	R
<31:24>	<i>mask</i>	Mask set version	R
<23:16>	<i>Reserved</i>	—	R
<15:8>	<i>maxtl</i>	Maximum trap level supported	R
<7:5>	<i>Reserved</i>	—	R
<4:0>	<i>maxwin</i>	Maximum number of windows of integer register file.	R

Fig 5

Downloaded by [REDACTED]



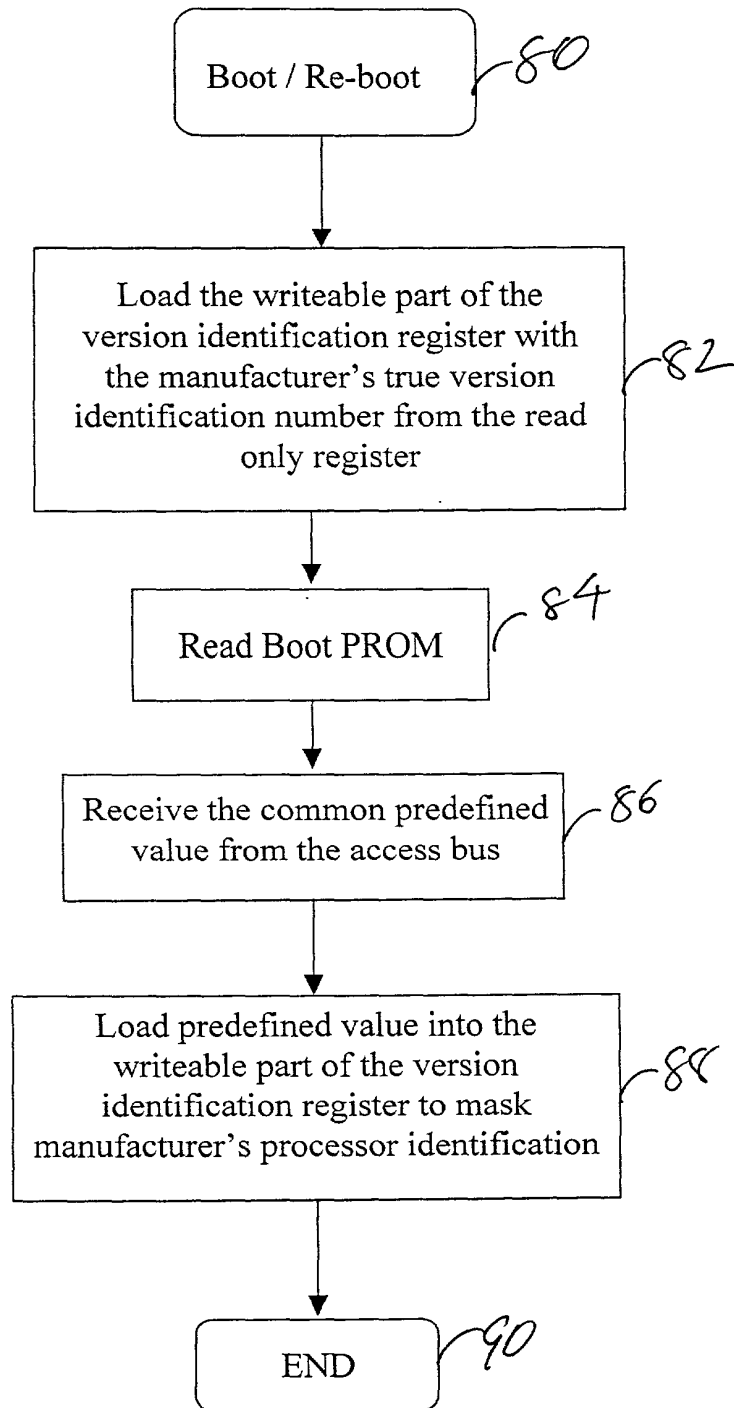


Fig 8